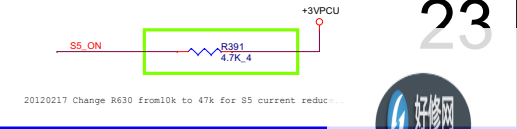
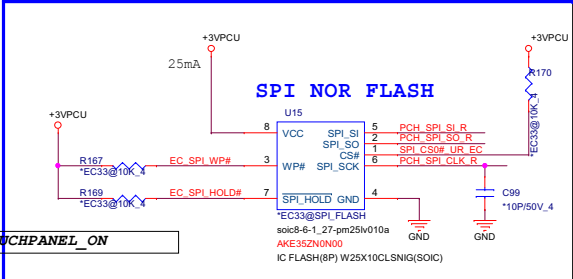
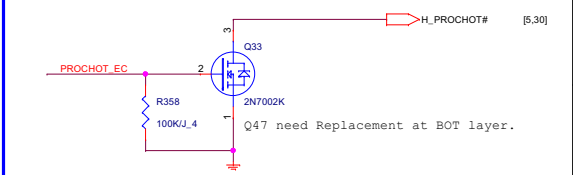
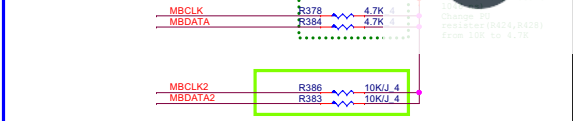


1.8V p/n: AJ009850F02
Discription: IC CONTROLLER (128P) NPCE985LB1DX (LQFP)

note:
GPIO75 EMU LIDTouch panel enable/disable#Follow ZEA -->ZHJ None
GPIO70 TP_EN_ECTouch pad enable/disable# -->ok
GPIO27 TP_INT_EC#Touch pad interrupt

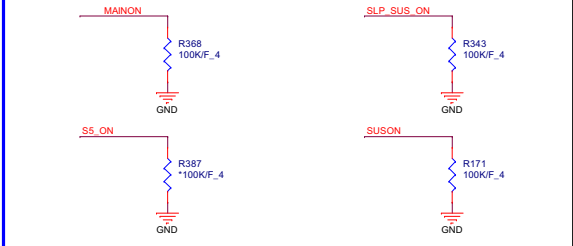
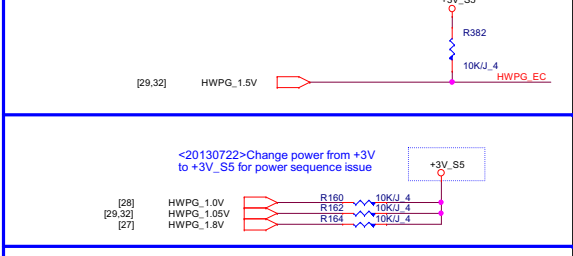


SM BUS PU(KBC)



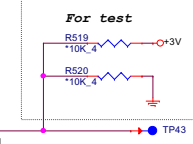
985L-A0 connects to +3VPCU / 985L-B1 connects to +1.8V_s5

HWPG(KBC)



985L-A0 connects to +3v / 985L-B1 connects to +1.8v

note:change EC p/n:
into AJ009850F02



Note: GPIO75 (pin82) for TOUCHPANEL_ON
pin91 in 985L is 1.8V only

<20090721_FAE suggestion>
Stuff 100K and close to EC side
for improving power consumption

SM BUS ARRANGEMENT TABLE

SM Bus 1	Battery
SM Bus 2	PCH
SM Bus 3	GPU

EC(KBC)

